ARMv8-M register DHCSR value on arm fast model with Trace 32

Awaiting Agent

- H HUGUET
- **Forum name:** #Debugging

Hello

I am running and debugging software built for ARMv8-M on a virtual platform using ARM Fast Models (11.27 version, using CADI protocol). Everything seems to be fine from the point of view of debugger functionalities. Anyway, if I am looking at the value of the CPU DHCSR register, the bit C_DEBUGEN keeps the value 0 (Halting Debug disabled). Even with this bit value, I am able to use breakpoints for example. When stopped on breakpoint, C_HALT & S_HALT bit seems to keep the 0 value ...) . So my question is the following : should Trace 32 write to the DHSCR register when connecting on an ARMv8-M processor ? (both for real CPU and model) . I thought this was one task to be done by the debugger to enable debug features on the processor when connecting, but I need confirmation of that to check if this register value is expected.

Many thanks for your support

Best Regards

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