CPU cores not responding to debug commands — JTAG and IDCODE detected Open Discussion

- H Hiba
- **Forum name:** #Board Bring-Up

I'm working with the S32G399A processor and TRACE32 for JTAG debugging. The JTAG interface is physically connected and functional — SYStem.DETECT DaisyChain successfully detects the IDCODE of the chip (0x1988c01d), indicating that the scan chain is intact.
However, when I attempt SYStem.Up or SYStem.Mode Attach, I consistently get the error: "Debug Port fail".
Additionally, the CPU cores are not responding to debug commands such as Data.List &0xE000EDF0.
Lowering JTAG clock frequency
Using SYStem.Option.EnReset OFF
Power cycling the board
Holding reset and attaching early
The target is powered, RESET_B is high, and the red LED no longer blinks when connecting JTAG.
Is this possibly related to clock gating, security lockdown, or missing start-up script? Any suggestions for enabling debug access to the cores or recommended script for S32G399A would be greatly appreciated.

Comment (1)

Wafi Jmal
1 year ago

Hello Hiba,

It appears that you are already using the latest release and the latest version of our script.

I’m not sure if you’ve made any modifications to the script.

But this could be a specific case. I recommend opening a new support ticket using the following link:
https://support.lauterbach.com/new-ticket

In your ticket, please provide a detailed description of the behavior.

Is this the first time you are working with this setup?

Additionally, please generate a System Information report about your TRACE32 configuration:

Go to Help > Support > System Information...

Click Save to File and attach the generated file to your email.

Also, please let me know:

Is the flash currently empty or does it contain data?

Any other relevant information about the setup or recent changes.

If the problem persists, we can arrange a remote session to investigate further.

Thank you.

Best Regards

Wafi