



[Support Center](#) > [Community](#) > [Debugging](#) > [How to debug Bus Reset in Infineon AURIX TC377TE device](#)

## How to debug Bus Reset in Infineon AURIX TC377TE device

### Awaiting Agent

- PR Partha Roy
- **Forum name:** #Debugging

Hello Support,

In the AREA Window as attached, I see Bus Reset.

Please provide me some hints about how to isolate the root cause of this.

Best regards

### Comments (2)

#### Houcem Dammak

2 months ago

Hello Pashan, For debugging reset I would first check the Reset Status Register of the microcontroller (SCU\_RSTSTAT). For TC377TE, you can check this register in the peripheral window: PER , "SCU (System Control Unit),RCU (Reset Control Unit)" However, from what I can see from the AREA window, there are m

#### Houcem Dammak

2 months ago

Hello Pashan,

For debugging reset I would first check the Reset Status Register of the microcontroller (SCU\_RSTSTAT).

For TC377TE, you can check this register in the peripheral window:

`PER , "SCU (System Control Unit),RCU (Reset Control Unit)"`

From the AREA window, it appears that communication between the debugger and the target is unstable. The target is not responding as expected, which causes the debugger to interpret the situation as if a reset is occurring.

A closer investigation of the debugger/target connection is required.

For better handling of your request, please open a support ticket and share the following information:

- Please generate a system information report about your TRACE32 configuration by selecting the TRACE32 menu 'Help' > 'Support' > 'System Information...', click 'Save to File' and send the resulting text file as an attachment to your e-mail.
- Share the startup scripts you are currently using.
- Please provide additional details about the target and adapters you are using. If possible, include a schematic snippet of your target board that shows how the debug header is connected to the microcontroller. If you are using a custom adapter, kindly share its schematic as well.

Best regards,

Houcem Dammak