how to select DM(Debug Module) for risc-v multi-cores arch cpu Awaiting Agent

- YW Yunguo Wu
- **Forum name:** #Debugging

Our chip has 2 rv cores, and every core has it's own DM. core0's DM base is 0x0, core1's DM base is 0x400. t32 can detect and attach to core0, but can't detect core1. Is there a t32 command to config the core1 's DM and attach to core1?

Comments (3)

Wiem Wala Benayed

4 months ago

This is still not supported in SMP mode. You can just use the AMP mode.

YW **Yunguo Wu**

4 months ago

Thanks for your reply! In AMP mode, how can i link to core1? The problem is that it seems T32 can't discovery the 2nd-DM which belongs to core1.

Wiem Wala Benayed

4 months ago

You can start TRACE32 to connect to the first core, then you need to use the command “TargetSystem.NewInstance” to connect to the second core.