



[Support Center](#) > [Community](#) > [Debugging](#) > [how to step.asm into interrupt routine on ARM Cortex-A53](#)

## how to step.asm into interrupt routine on ARM Cortex-A53 **Awaiting Agent**

- 王 王洪柱
- **Forum name:** #Debugging

Hi,

Without breakpoints, is there any method to step.asm into interrupt routine?

### Comments (3)

**Fatma Masmoudi**

9 months ago

Hello,

Can you please explain your use-case?

Are you trying to enter an interrupt routine and then step into the assembly instructions?

If that is the case, can you please explain why breakpoints are not a convenient solution for you ?

王 王洪柱

9 months ago

Hi[] when I step the assembly instruction 'msr daifclr,#0x3' which is to enable FIQ and IRQ[]a register in my SoC cannot be accessed any more and Trace32 cannot be stopped by pressing the 'break' button. I guess something is wrong in the interrupt routine[]but after breakpoints are set at interrupt vectors entry, Trace32 didn't stop on these breakpoints at all when stepping the above instruction. I want to know where the PC goes after the step operation.

**Fatma Masmoudi**

9 months ago

Hello,

My understanding is that when trying to enable IRQs and FIQs the core cannot be stopped anymore.

We will probably need more information to troubleshoot this issue.

That's why I have opened a support ticket to deal with your request to avoid disclosing any confidential information.

Please confirm that you have successfully received an email from "[support@lauterbach.com](mailto:support@lauterbach.com)".