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j784s4-cr5-mcu_ddr.cmm hang on TI J784S4 EVM board **Awaiting Agent**

- Jim Zhang
- **Forum name:** #Debugging

The DDR PLL configuration cause board spinning forever

Comment (1)

Ahmed Ghribi

6 months ago

Hello Jim, This is part of our script based board setup demo, it expects a General Purpose target and the boot mode is set to "No Boot". I have sent a private email for further details. Best regards,Ahmed Ghribi