NXP İ.MX95 eval Board Debugging Open Discussion

- RD Rabia DOĞAN
- **Forum name:** #Debugging

Hi Dear,

We are currently setting up our debug environment for the NXP i.MX95 Evaluation Board. Our primary objective is to debug the Cortex-M7 and Cortex-M33 cores using TRACE32.

Could you please provide guidance and the recommended .cmm start-up scripts for the following scenarios:

Attaching to and debugging the Cortex-M7 core independently.

Attaching to and debugging the Cortex-M33 core independently.

Multicore debugging (AMP) setup to debug both the M7 and M33 cores simultaneously (if applicable/supported).

Additionally, please let us know if there are any specific hardware considerations (e.g., specific JTAG/SWD routing configurations on this eval board) or minimum TRACE32 software version requirements for full i.MX95 support.

System Details:

Target Board: NXP i.MX95 Evaluation Board

Target Cores: ARM Cortex-M7, ARM Cortex-M33

Debugger Hardware: [PowerDebug E40 / LA-3000 DCABLE]

Thank you in advance for your support.

Best regards,
Rabia DOĞAN

Comment (1)

Khaled Jmal
4 months ago

Hello Rabia,

I see that you have also submitted a ticket for this request. We will follow up with you shortly through the ticket system.

Regards,

Khaled