Query on Code coverage for NXP S32M244 Open Discussion

- k kotrike.adithya
- **Forum name:** #Trace Analysis

HI,
Could you please let me know how code coverage for functions and branches can be achieved on the NXPS32M244 controller using Lauterbach TRACE32 (Cortex-M debugger).
I would like to understand whether this is supported with the GHS compiler and what prerequisites are required for the setup.
Additionally, please share the detailed procedure to enable coverage and generate the coverage report using TRACE32.

Thank you in advance.

Comments (3)

Wiem Wala Benayed
10 months ago

Yes, code coverage for functions and branches is fully supported on Cortex-M processors with GHS compiler. TRACE32 supports trace-based code coverage using the ETMv4.

Please check our documentation for more details about the necessary steps:
https://repo.lauterbach.com/pdf/app_code_coverage.pdf

k **kotrike.adithya**
9 months ago
Hi, Based on the technical specifications of the NXP S32M244, it appears that hardware-based code coverage is restricted due to processor or chip-level limitations. Could you please confirm if there are alternative methods to achieve code coverage using Lauterbach TRACE32? Specifically, I am interested in knowing if software-based instrumentation or other trace-based workarounds are available for this variant. I sincerely appreciate your efforts. Thank you.
Wiem Wala Benayed
9 months ago

TRACE32 supports trace-based code coverage through on-chip or off-chip trace interfaces. To enable code coverage measurement, your target processor must provide trace capability.