T2080 OTPMK Fuse Programming Open Discussion

- VS Vismay Sharma
- **Forum name:** #Board Bring-Up

Hi

I am trying to perform OTPMK fuse programming for T2080 RDB via trace32 debugger. As per NXP recommendations the pre fusing steps have been performed. The debugger when put in prepare mode for the fusing process doesnt allow writing or reading any register location.

Also for T2080 the ANC or HSD is not a secure class. What are the steps to fuse the OTMPK via trace32 and what access class must be used and how.

Thanks & Regards
Vismay

Comments (3)

Wafi Jmal
2 years ago
Hello, Please refer to this script "sdc_secure_device.cmm" under "~~\demo\powerpc64bit\hardware\qoriq_misc." Be sure to read the header of the script. Regards.
VS **Vismay Sharma**
2 years ago
Hi Wafi I went through the script and what I understood is that the script is targeted towards secure debug response post key fusing. We are currently in the process of fusing the keys itself. What I gather is that the DBG class is being used to access the SFP registers? In prepare mode we arent able to access any class and dont know which class to use for targeting OTPMK mirror registers. What access class should be used for the same? Also when fusing OTPMK anything else that needs to be done other than that what is mentioned in the NXP manual. All clocks and pins are configured already for the same. Thanks Vismay
Wafi Jmal
2 years ago
Hello, We have no example or configuration Script for programming the OTPMKRx registers. I can only suggest that they use the sdc_secured_device.cmm script as a template. The registers SFP_DCVRx and SFP_DRVRx use

only other address offsets, so in my opinion this script uses the same mechanisms to secure the Debug Controller.