



[Knowledgebase](#) > [FAQs by core architecture](#) > [ARC](#) > [\[ARC\] How can I see the auxiliary registers?](#)

## [ARC] How can I see the auxiliary registers?

2025-09-16 - [Comments \(0\)](#) - [ARC](#)

The auxiliary register space is accessible in PowerView via the special memory class "AUX".

To view the registers, simply type:

```
Data.dump AUX:0
```

For any ARC700 core, lots of registers will be displayed with ????????, which means that those registers are inaccessible, because they are not present at your core.

For ARC600 and older cores, unimplemented auxiliary registers normally show the same value as the IDENTITY register.

The most common auxiliary register are also shown in the peripheral view. Open the peripheral view with the command **PER.view**.