

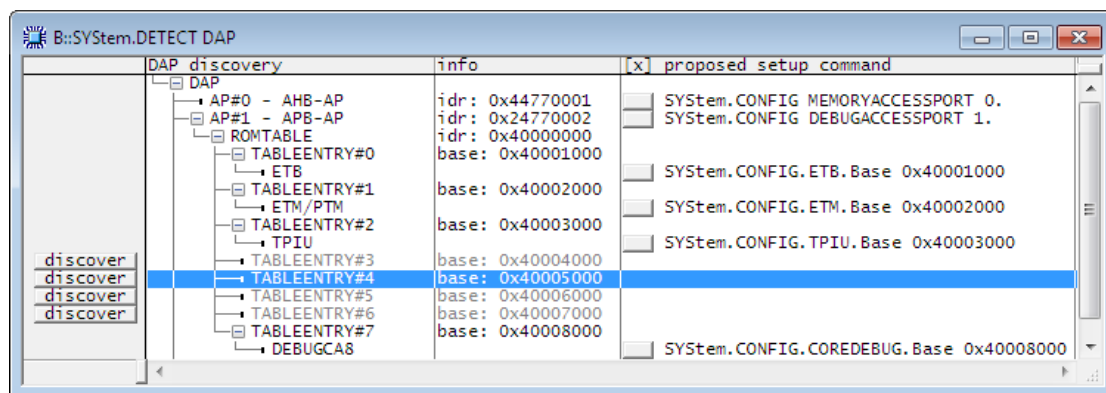
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The ROM table can be scanned in TRACE32 using the command

SYStem.DETECT DAP



However, TRACE32 does not rely on the ROM table. If the chip is supported by TRACE32, then it is enough to select the right CPU using the command

SYStem.CPU < cpu >

Otherwise, the CoreSight settings have to be set up with a script using the **SYStem.CONFIG** commands. Refer in this case to the document [Setup of the Debugger for a CoreSight System](#)

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