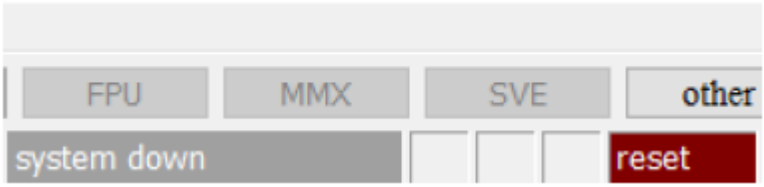
[Arm] SYStem.Up / SYStem.Mode Attach returns "target reset detected"

2026-07-31 - Comment (1) - Arm

This message indicates that TRACE32 detected an **active reset signal** on the target while attempting to establish the debug connection.



Troubleshooting

Check the following:

- Verify the **nRESET** signal on the debug connector using a voltmeter or an oscilloscope. Make sure the reset line is not permanently asserted or being periodically driven low.
- Power off the target, disconnect the debug cable, reconnect it, and then power the target back on. It is generally recommended to connect or disconnect the debug cable only while the target is powered off.
- If appropriate for your hardware, disable reset detection before connecting using the command `SYStem.Option ResetDetection NONE`
- Verify that the target power supply is stable and capable of providing sufficient current. An unstable or undervoltage power supply can repeatedly assert the reset signal and lead to this error.
- Check the target hardware for external reset sources, such as a reset supervisor, watchdog timer, or another device that may be keeping the processor in reset.

If the reset signal is asserted intentionally by the target hardware or firmware, identify and eliminate the cause before attempting to connect again.

Comment (1)

Comment (1)

Nenavath Hari
1 year ago
not resolving still in RESET only