Does RH850 targets support memory access during runtime?

2026-09-03 - Comments (0) - RH850

All RH850 devices support memory access during runtime using the command **SYStem.MemAccess CPU.**

So It is possible to read from memory, to write to memory and to set software breakpoints while the CPU is executing the program.

During "running" mode:
- Software breakpoints can be set/modified.
- Onchip breakpoints cannot be set/changed. This is RH850 chip restriction.
With the command **SYStem.CpuBreak Enable**, TRACE32 will do a quick STOP→ Set/Clear Breakpoints → GO when breakpoints are set/changed.
- Global variables can be viewed and modified.