

Knowledgebase > Flash programming > Erased FLASH cells are flickering or do not show the default erase value

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Reading an internal FLASH cell that is in erased state will result in random and sometimes flickering data. This is generally due to an address dependent ECC for the internal FLASH. So, if the FLASH and the FLASH ECC are cleared, then the ECC values 0xFF do not match the cleared FLASH 0xFF and result in bit error corrections.