



Knowledgebase > Tracing > Is it possible to group multiple logic inputs (L00-L11) together to have a parallel bus using the Mixed-Signal Probe?

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This can be done using the command **NAME.Word**

Example:

NAME.Word data CIProbe.00 CIProbe.01 CIProbe.02 CIProbe.03

NAME.Set CIProbe.04 clk

NAME.Set CIProbe.05 valid

CIProbe.Timing CIProbe.CLK CIProbe.VALID Word.DATA

Refer for more information to

- [general_ref_c.pdf](#) example for CIProbe.TSYNC.SELECT
- [general_ref_n.pdf](#) chapter NAME.Word