Is it possible to group multiple logic inputs (L00-L11) together to have a parallel bus using the Mixed-Signal Probe?

2026-08-13 - Comments (0) - Tracing

This can be done using the command **NAME.Word**
Example:

```
NAME.Word data CIProbe.00 CIProbe.01 CIProbe.02 CIProbe.03
NAME.Set CIProbe.04 clk
NAME.Set CIProbe.05 valid
CIProbe.Timing CIProbe.CLK CIProbe.VALID Word.DATA
```

Refer for more information to

- general_ref_c.pdf example for `CIProbe.TSYNC.SELect`
- general_ref_n.pdf chapter `NAME.Word`