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Is it possible to set an on-chip breakpoint on a physical address for a processor with enabled MMU?

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On-chip breakpoints trigger on addresses used by the core. If the Memory Management Unit (MMU) is enabled, the core uses virtual addresses. It is thus not possible to set an on-chip breakpoint using the physical address. Please note that specifying the memory class **A:** has no effect, the given address will be simply considered as virtual address. You should instead find all virtual addresses that are mapped to the given physical address and set an on-chip breakpoint on each virtual address. This won't work however in case the address mapping is created after setting the breakpoint.

The TRACE32 command **MMU.INFO < address >** can be used to find all virtual to physical address mappings for a given physical address.

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