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## **[MPC85xx][Linux] Breakpoint are not working properly for e500 cores**

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The Linux kernel for e500 cores (PQ3/MPC85XX/QorIQ P10XX/P2020) has to be patched to enable debugging via JTAG. The MSR\_KERNEL macro defined in arch/powerpc/include/asm/reg\_book.h needs to be changes to include the MSR\_DE bit:

```
#define MSR_KERNEL (MSR_ME|MSR_RI|MSR_CE|MSR_DE)
```