

Knowledgebase > FAQs by core architecture > RISC-V > [RISC-V] [HW-Designer] Which ways of integrating a RISC-V debug module into a SoC are currently supported?

[RISC-V] [HW-Designer] Which ways of integrating a RISC-V debug module into a SoC are currently supported?

2023-05-09 - Comments (0) - RISC-V

Please see the Lauterbach <u>RISC-V Debugger</u> manual, chapter "Quick Start for Debug Module Configuration" for details.

Tags	
Hardware Designer	
RISC-V	