[RISC-V] Which RISC-V base ISAs are supported?

2026-10-07 - Comments (0) - RISC-V

The RISC-V specification defines base ISAs for 32bit (RV32), 64bit (RV64) and 128bit (RV128). The Lauterbach debug driver generally supports core architectures with RV32 and RV64 base ISA. However, RV128 ISA cores are currently not supported.

- Tags
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