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## Trace support for PolarFire® SoC (RISC-V + FPGA)

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Please refer to [https://gitlab.com/lauterbach/polarfire\\_trace](https://gitlab.com/lauterbach/polarfire_trace). The repository contains resources to implement off-chip trace of the hard RISC-V cores of a PolarFire® SoC with Lauterbach tools.