Trace support for PolarFire® SoC (RISC-V + FPGA)

2026-04-03 - Comments (2) - RISC-V

Please refer to https://gitlab.com/lauterbach/polarfire_trace.

The repository contains resources to implement off-chip trace of the hard RISC-V cores of a PolarFire® SoC with Lauterbach tools.

Comments (2)

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Nigel
11 months ago
Comes up with 404 page not found
Khaled Jmal
11 months ago

Hello Nigel, the link should be fixed now.

Regards,

Khaled