Trace support for PolarFire® SoC (RISC-V + FPGA)

2025-11-14 - [Comments (2)](#) - [RISC-V](#)

Please refer to [https://gitlab.com/lauterbach/polarfire_trace.](https://gitlab.com/lauterbach/polarfire_trace)

The repository contains resources to implement off-chip trace of the hard RISC-V cores of a PolarFire® SoC with Lauterbach tools.

Comments (2)

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N **Nigel**
3 months ago
Comes up with 404 page not found
Khaled Jmal
3 months ago

Hello Nigel, the link should be fixed now.

Regards,

Khaled