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## Trace support for PolarFire® SoC (RISC-V + FPGA)

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Please refer to [https://gitlab.com/lauderbach/polarfire\\_trace](https://gitlab.com/lauderbach/polarfire_trace).

The repository contains resources to implement off-chip trace of the hard RISC-V cores of a PolarFire® SoC with Lauterbach tools.

### Comments (2)

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**N Nigel**

5 months ago

Comes up with 404 page not found

**Khaled Jmal**

5 months ago

Hello Nigel, the link should be fixed now.

Regards,

Khaled