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Both TriCore AGBT trace converters (LA-3829 for Serial Trace Preprocessor and LA-3556 for Power Trace Serial) are routing the WDTDIS/OCDSE signal from the debug connectors to the TRIG pin.

Then the debugger can e.g. use this to disable an external watchdog via the command:

SYSTEM.CONFIG EXTWDTDIS

For more details please refer to the following paragraphs in the document [Application Note Debug Cable TriCore](#).

- Controlling an External Watchdog
- Converter 16-pin OCDS-L1/ 40-pin HSSTP to ERF8 for TriCore
- Converter OCDS-L1/ AUTO26/ PowerTrace Serial to ERF8 for TriCore