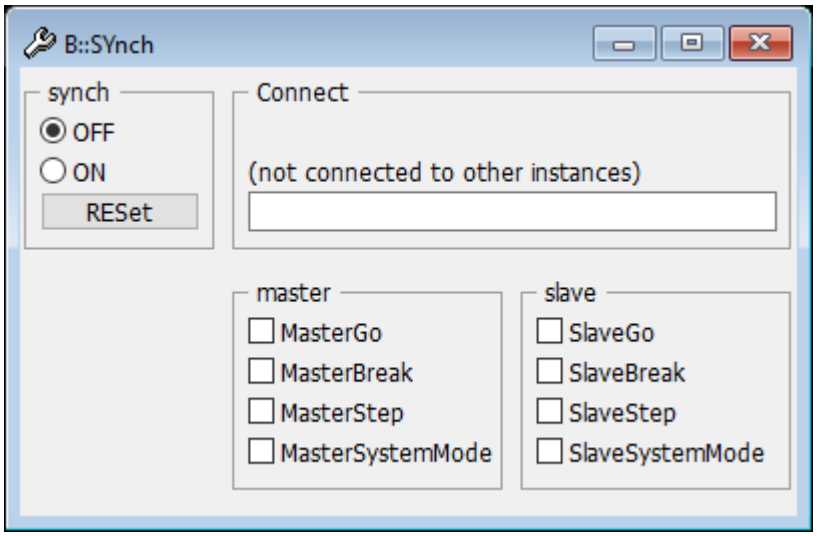
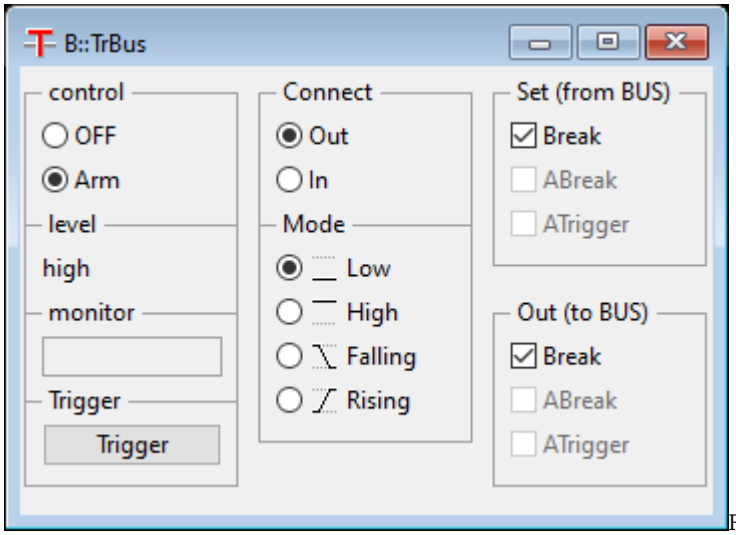
We debug two chips over two JTAG connector with two debug modules. Is it possible to stop both chips synchronously?

2026-04-03 - Comments (0) - Setup / update

You can use the **sYnch** window, then the synchronization is done by software with an approximate delay of 10 milliseconds.



Otherwise, you can get a hardware-based synchronization if the chip supports BDGREQ and DBGACK signals. In this case, you can connect both debug modules over PODBUS and use the commands **TrBus.Out Break ON** and **TrBus.Set Break ON** for each PowerView instance. This type of synchronization is however not supported for synchronous start (**Go**).



Refer for more information to the documentation of these commands in General Commands Reference Guide T.