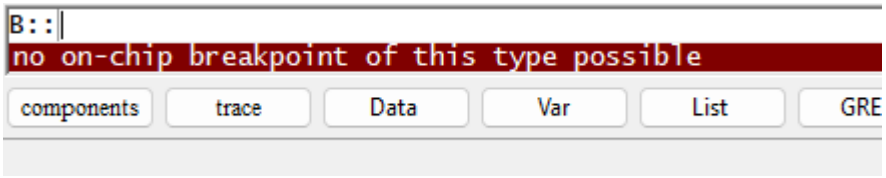
What does the error message "no on-chip breakpoint of this type possible" mean?

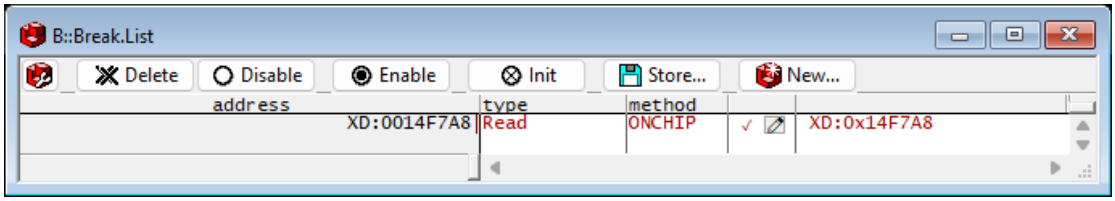
2026-08-13 - Comments (0) - TRACE32 PowerView

This error appears when you attempt to set an on-chip breakpoint that is not supported by the processor's on-chip debug hardware in the current system state or architecture.



In other words, the requested breakpoint type cannot be provided by the hardware resources available on your target.

In the **Break.List** window, the breakpoint will be shown in **red**.



If you try to resume execution, you will see the error **"breakpoint configuration invalid."** To resolve the issue, simply remove the unsupported breakpoint.

For details on which breakpoint types are supported, please consult the *Processor Architecture Manual* of your target architecture (see main.pdf > *Processor Architecture Manuals*).