TRACE32 Instruction Set Simulator and ISO 26262

2022-12-15 - Comments (0) - Functional Safety

ISO 26262 permits processor emulators like the *TRACE32 Instruction Set Simulator* as tools for software unit and module integration testing. This makes it possible for Lauterbach to offer tools for the complete life cycle of automotive projects.

- **TRACE32 Instruction Set Simulator**
  for software unit and module integration testing before the first device samples are available and for additional test capacities during the project lifetime.
- **TRACE32 Hardware-Assisted Debug and Trace Tools**
  for all project phases, after evaluation boards or target hardware are available.
- **TRACE32 XCP Debug and Trace Tools**
  for final system testing after the target hardware is deeply embedded in the car.

The same qualification guidelines apply to the *TRACE32 Instruction Set Simulator* as to other software tools. Users should determine early in the project life cycle for which activities the TRACE32 Instruction Set Simulator should be used. Following the qualification guidelines ensures that it can be used with confidence. Lauterbach offers a *TRACE32 Tool Qualification Support Kit Simulator TriCore* to simplify the qualification process. Kits for other core architectures are in preparation.

Qualification is not the only option. Confidence in the use can be justified by comparing the test results of the *TRACE32 Instruction Set Simulator* with reference tests, e.g. on virtual targets or software-in-the-loop tests on the host computer.

Please refer to the fact sheet *TRACE32 Instruction Set Simulator and ISO 26262* for more details on this topic (trace32iss_iso26262.pdf).

Minimum software: -

Supported core architectures: TriCore™ AURIX™ TC2x/TC3x/TC4x

Attachments

- trace32iss_iso26262.pdf [379.57 KB]